

IFW

Gp 2819



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Om P. Agrawal; Jinghui Zhu
Assignee: Lattice Semiconductor Corporation
Title: Block Oriented Architecture For A Programmable Interconnect Circuit
Serial No.: 10/022,464 Issue Date December 14, 2001
Examiner: Vibol Tan Group Art Unit: 2819
Docket No.: M-9642 US Confirmation No. 1715

Irvine, California
March 24, 2006

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria VA 22313-1450

**RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT
(37 CFR 1.121)**

Dear Sir:

In response to the Notice of Non-Complaint Amendment (37 CFR 1.121) mailed by the United States Patent and Trademark Office on February 13, 2006, the following documents have been corrected and are being resubmitted:

1. Nine (9) Sheets of Replacement Drawings, and
2. Copy of Notice to of Non-Compliant Amendment (37 CFR 1.121).

The Commissioner is hereby authorized to charge any additional fees, which may be required, or credit any overpayment to Deposit Account No. 50-2257.

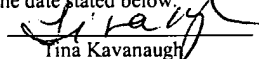
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It is hereby respectfully submitted that the enclosed documents correct the noted informalities. Please telephone the undersigned at (949) 752-7040, if there are any questions.

This form is being submitted in duplicate.

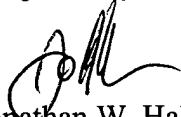
Certificate of Mailing

I hereby certify that this correspondence is being sent via First Class mail to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, MAIL STOP AMENDMENT on the date stated below.


Tina Kavanaugh

March 24, 2006

Respectfully submitted,


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